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TSMC 7nm vs Intel 10nm Density

Another video
addressing the
misinformed trolls...
well idk if they are
trolls, but they
certainly are

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misinformed. TSMC's ...

***GlobalFoundries
Sues TSMC for
Patent Infringement
| Steam Bug Bounty
Expands After Flaw
Dismissed***

GlobalFoundries are
suing **TSMC** (and
others like Apple,
Qualcomm and Nvidia)
over patent
infringement of several
of **it's** ...

Samsung Foundry's
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New Transistor

Structure: MBCFET™

Samsung's patented version of Gate-All-Around, MBCFET™ (Multi-Bridge-Channel FET), uses a nanosheet architecture, which ...

Introduction to

FinFETs Introduction to FinFETs, how do you define the device width
Stanford University's class on nanotech, led by Aneesh Nainani.

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From Sand to Silicon: the Making of a Chip | Intel This

is how a microprocessor, the brain 'behind the magic' of your PC, is made. For more about process Intel employs in building ...

5 nanometer transistor -- how they did it IBM and its Research Alliance partners
GLOBALFOUNDRIES

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and Samsung have developed a first-of-a-kind process to build ...

TSMC: Taiwan's Most Valuable Company and Apple's Crucial Strategic Partner

This video is about Taiwan's biggest company and its crucial strategic relationship to Apple. It is the one of the top 20 ...

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Designing 7-nm IP, Bring It On Moore! In keeping with Moore's Law, discover how Synopsys is developing 10nm/7nm IP for SoC designs. Learn how tradeoffs are made ...

Stanford Engineering Hero Lecture: Morris Chang in conversation with President John L. Hennessy In this lecture, Stanford

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University President
John L. Hennessy leads
a discussion with
Stanford Engineering
Hero Morris Chang, ...

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***The Future of
Semiconductor Chips***

Eugene Fitzgerald,
chief executive officer
at Singapore-MIT
alliance for research
and technology,
discusses the
development of ...

***Multi-Physics At
5/3nm*** Joao Geada,
chief technologist at
ANSYS, talks about why
timing, process,
voltage, and
temperature no longer

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can be considered ...

How a CPU is made

How a CPU is Made -
CPU Manufacturing
Central Processing Unit
#CPU Global Foundries
shows how a CPU is
made with all ...

***What's inside a
microchip ?*** In this
video I take you on a
tour of the visible
internal workings of a
27C512 EPROM
microchip, with the aid
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of an optical ...

***The Most
Sophisticated
Manufacturing
Process in the World***

| Inside the Fab |

Intel Meet John
Breseke, Director of
Manufacturing **IT** at
Intel whose job **it** is to
manage the **IT** and
automation of arguably
the most ...

***This Is the End of
the Silicon Chip,***

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Here's What's Next

**Intel: The Making of
a Chip with 22nm/3D
Transistors | Intel**

This video shows the process of how computer chips are made using Intel's world leading 22nm manufacturing **technology** with ...

**ARM 16/14nm
FinFET
Manufacturing
Leadership** John
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Heinlein, Vice President of Marketing, Physical IP Division at ARM talks about the 14nm FinFET ARM Processor ...

Chip makers consolidating

Cypress Semiconductor CEO T.J. Rodgers discusses China's markets and the chip industry. Watch David Asman talk about **Tech** ...

Profiling Taiwan's

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Biggest Company:

Part 1 TSMC or Taiwan Semiconductor Manufacturing

Company is arguably Taiwan's crown jewel. It bears the pride of the nation and is ...

High Aspect Ratio structures in semiconductor chips

Process challenges associated with etching and filling high aspect ratio structures in semiconductor chips.

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Stanford University's ...

**A manufacturing
tour** TI Investor
Relations http://www.ti.com/corp/docs/investor_relations/index.html
See our 300-millimeter
chip manufacturing, ...

**IBM's 5nm
Transistor
Technology can
extend Mobile
Battery Life 2 or 3
Times | QPT** IBM , its
Research Alliance
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partners

GLOBALFOUNDRIES
and Samsung, and
equipment suppliers
have developed an
industry-first ...

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